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2	. S IN THE	UNITED STA	TES PATE	NT AN	D TRADEMAR	FICE	PATENT APP	PLICATION
	5 2013 B				Group Art U	nit 2826		Drof
	Inventor(s) INABA				Examiner:	Dickey, The	omas L.	LXL
		042,264			Atty. Dkt.	P 284163	01F181	
متر	Series Code 1	Serial No. 个			A 4 75741	M#	Client Ref	
	Filed: January 11, 2002				Appln. Title:			IN PINE
	Hon. Commissioner of Patents					REALIZING CHA	ARAISI ERISI	ICS-FINE
	Washington, D.C. 20231					A SOI MOSFET	10 7	CENED OF THE
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	Sir:				Date:	April 25, 2003	MOLDEY CENTE	
	REPLY/AMENDMENT/LETTER				Date.	April 20, 2005		CENED 2003
	This is a reply/amendment/letter in the above-identified application and includes the herewith attachment of same date and subject							
	which is incorporated hereinto by ref	erence and the	signature be	elow is	treated as the sig	nature to the attack	hment in ab��	nce of a
	signature thereto.						-	,
_		FEE REQUI	REMENTS F	OR CL	<u>AIMS AS AMEN</u>	<u>DED</u>		
	1. Small Entity claim							
	A. NOT made For B & C	Claims remaining after	Highest nur previously pa		Present Extra	Large/Small Entity	Additional Fee	Fee Code
	made herewith Separate Paper	amendment	previously paid for				100	Lg/Sm
	D. made previously (Pat-256)		ļ.					
L								
	2. Total Effective Claims	46	**minus	46	0	x \$18/\$9 =	+ \$0	103/203
	3. Independent Claims	10	***minus	10	0	x \$84/\$42 =	+ \$0	102/202
	4. If amendment enters proper multip						. 60	101/001
	time (leave blank if this is a reissue application)				add	+ \$280/\$140 =	+ \$0	104/204
	5. Original due Date: April 30, 2003				0440/055			115/215
ĺ	6. Petition is hereby made to extend the original due (1 mo)				\$110/\$55 =	+ \$0		115/215 116/216
	date to cover the date this response is filed for which the (2 mos)				\$410/\$205 = \$930/\$465 =	+ \$0		117/217
	requisite fee is attached (3 mos) (4 mos)				\$1,450/\$725=		Design 1	118/218
				mos)	\$1,970/\$985=			128/228
╟	7. Enter any previous extension fee <u>paid</u> since above <u>original</u> due date ar					- \$0		
╟	8.					Extension Fee	+ \$0	
и	9. If Terminal Disclaimer attached, add Rule 20(d) official fee					+ \$110/\$55	+ \$0	148/248
r	10. If IDS attached requires Official F					+ \$180	+ \$0	126
и			• , .		hhe	+ \$180	1 + 20	126
	or if Rule 97(d) Request	••••••		• • • • • • • • •	auu			
	or if Rule 97(d) Request11. After-Final Request Fee per rule	s 129(a) and 17	7(r)			+ \$750/370	+ \$0	146/246
ļ.		s 129(a) and 17	7(r)			+ \$750/370 x \$750/375 ea	+ \$0	149/249
	11. After-Final Request Fee per rule	s 129(a) and 17 xamination per	7(r) Rule 129(b).			+ \$750/370	+ \$0 + \$0	149/249
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	 11. After-Final Request Fee per rules 12. No. of additional inventions for e 13. Request for Continued Examinat 14. Petition fee for 15. 	s 129(a) and 17 xamination per ion (RCE)	7(r) Rule 129(b).			+ \$750/370 x \$750/375 ea	+ \$0 + \$0	149/249
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APR 2 5 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

in re PATENT APPLICATION OF

Confirmation No.: 6258

INABA

Group Art Unit: 2826

Appln. No.: 10,042,264

Examiner: Dickey, Thomas L.

Filed: January 11, 2002

Title: SEMICONDUCTOR DEVICE REALIZING CHARACTERISTICS LIKE A

MOSFET (As Amended)

April 25, 2003

AMENDMENT

Hon. Commissioner of Patents Washington, D.C. 20231

Sir:

In response to the Office Action dated January 30, 2003, please enter the following amendments and remarks:

IN THE TITLE:

Please replace the Title of the Disclosure with the following new Title:

SEMICONDUCTOR DEVICE REALIZING CHARACTERISTICS LIKE A SOI MOSFET

Please see the attached specification for the changes made to effect the title.

IN THE CLAIMS:

Please amend claim 1 as follows:

- 1. (Amended) A semiconductor device comprising:
- a semiconductor substrate having a surface;
- a gate electrode formed over the surface of said semiconductor substrate with a gate dielectric film interposed therebetween;

a pair of source and drain diffusion layers formed in said semiconductor substrate to oppose each other with a channel region laterally residing therebetween at a location immediately beneath said gate electrode, said source and drain diffusion layers each having a low resistivity region and an extension region being formed to extend from this low resistivity

